

TIP100/101/102

Monolithic Construction With Built In Base-Emitter Shunt Resistors

- High DC Current Gain : h_{FE}=1000 @ V_{CE}=4V, I_C=3A (Min.)
- Collector-Emitter Sustaining Voltage
- Low Collector-Emitter Saturation Voltage
- Industrial Use
- Complementary to TIP105/106/107

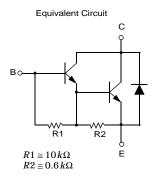


1.Base 2.Collector 3.Emitter

NPN Epitaxial Silicon Darlington Transistor

Absolute Maximum Ratings T_C=25°C unless otherwise noted

Symbol	Parameter	Value	Units	
V _{CBO}	Collector-Base Voltage : TIP100	60	V	
	: TIP101	80	V	
	:TIP102	100	V	
V _{CEO}	Collector-Emitter Voltage: TIP100	60	V	
	: TIP101	80	V	
	: TIP102	100	V	
V _{EBO}	Emitter-Base Voltage	5	V	
I _C	Collector Current (DC)	8	Α	
I _{CP}	Collector Current (Pulse)	15	Α	
I _B	Base Current (DC)	1	Α	
P _C	Collector Dissipation (T _a =25°C)	2	W	
	Collector Dissipation (T _C =25°C)	80	W	
T _J	Junction Temperature	150	°C	
T _{STG}	Storage Temperature	- 65 ~ 150	°C	



Electrical Characteristics T_C=25°C unless otherwise noted

Symbol	Parameter	Test Condition	Min.	Max.	Units
V _{CEO} (sus)	Collector-Emitter Sustaining Voltage				
	: TIP100	$I_C = 30 \text{mA}, I_B = 0$	60		V
	: TIP101		80		V
	: TIP102		100		V
I _{CEO}	Collector Cut-off Current				
	: TIP100	$V_{CE} = 30V, I_{B} = 0$		50	μΑ
	: TIP101	$V_{CE} = 40V, I_{B} = 0$		50	μΑ
	: TIP102	$V_{CE} = 50V, I_{B} = 0$		50	μΑ
I _{CBO}	Collector Cut-off Current				
	: TIP100	$V_{CE} = 60V, I_{E} = 0$		50	μΑ
	: TIP101	$V_{CE} = 80V, I_{E} = 0$		50	μΑ
	: TIP102	$V_{CE} = 100V, I_{E} = 0$		50	μΑ
I _{EBO}	Emitter Cut-off Current	$V_{EB} = 5V, I_{C} = 0$		2	mA
h _{FE}	DC Current Gain	$V_{CE} = 4V, I_{C} = 3A$	1000	20000	
		$V_{CE} = 4V$, $I_C = 8A$	200		
V _{CE} (sat)	Collector-Emitter Saturation Voltage	$I_{C} = 3A, I_{B} = 6mA$		2	V
		$I_{C} = 8A, I_{B} = 80mA$		2.5	V
V _{BE} (on)	Base-Emitter ON Voltage	$V_{CE} = 4V, I_{C} = 8A$		2.8	V
C _{ob}	Output Capacitance	$V_{CB} = 10V, I_{E} = 0, f = 0.1MHz$		200	pF

Typical Characteristics

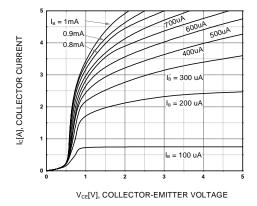


Figure 1. Static Characteristic

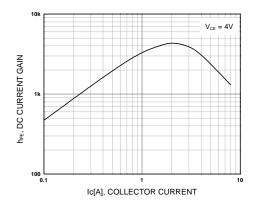


Figure 2. DC current Gain

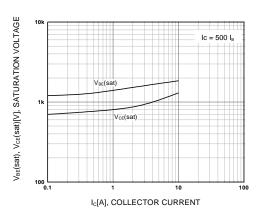


Figure 3. Collector-Emitter Saturation Voltage Base-Emitter Saturation Voltage

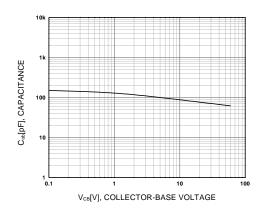


Figure 4. Collector Output Capacitance

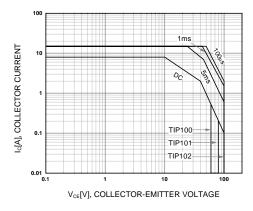


Figure 5. Safe Operating Area

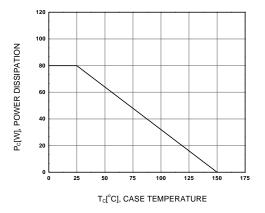
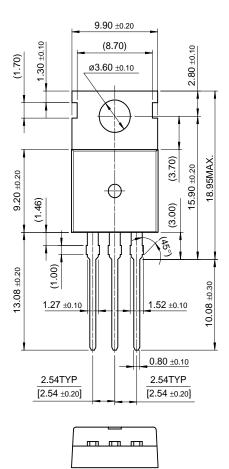


Figure 6. Power Derating

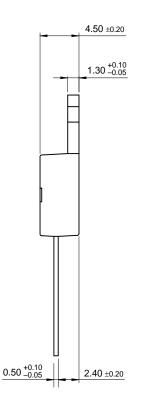
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Package Demensions

TO-220



 10.00 ± 0.20



Dimensions in Millimeters

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